AMENDMENTS TO THE CLAIMS

Claim 1 (Currently Amended): A liquid crystal display comprising:

5 a plurality of signal lines;

10

15

- a plurality of scanning lines electrically connected to a scanning line control circuit; and
- a plurality of pixels, each pixel comprising:
 - a liquid crystal cell having a pixel electrode and a storage capacitor, and
 - a switching transistor comprising a gate electrode connected to a scanning line, a drain electrode connected to one of the [[a]] signal lines, and a source electrode connected to the pixel electrode, the gate electrode and the source electrode having an overlapping region;
- wherein an area of the overlapping region of a pixel

 closer to the scanning line control circuit is

 smaller than an area of the overlapping region
 of another pixel farther from the scanning line
 control circuit an area of an overlapping region
 of the gate electrode and the source electrode
 is increased by increasing a distance between an
 input and of the scanning line corresponding to
 the overlapping region and the pixel
 corresponding to the overlapping region.
- Claim 2 (Currently Amended): The liquid crystal
 display of claim 1 wherein the gate electrode of
 each pixel comprises a first block located within
 the overlapping region, and an area of the first

5

30

block of a pixel closer to the scanning line control circuit is smaller than an area of the first block of another pixel farther from the scanning line control circuit an area of the first block is increased by increasing the distance.

Claim 3 (Currently Amended): The liquid crystal display of claim 1 wherein the source electrode comprises a second block of each pixel located within the overlapping region, and an area of the second block of a pixel closer to the scanning line control circuit is smaller than an area of the second block of another pixel farther from the scanning line control circuit an area of the second block is increased by increasing the distance.

Claim 4 (Original): The liquid crystal display of claim

2 wherein the gate electrode further comprises a
pair of protective structures located on both sides

of the first block for preventing the first block
from being separated from the gate electrode.

Claim 5 (Currently Amended): A liquid crystal display comprising:

- 25 a scanning line connected to a scanning line control circuit;
 - a first region of the scanning line comprising at least a first transistor, which has having a first gate electrode connected to the scanning line, a first drain electrode connected to a first signal line, and a first source electrode connected to a first pixel electrode, wherein

a first overlapping region emists between the first gate electrode and the first source electrode having a first overlapping region; and

- 5 a second region located between the scanning line control circuit and the first region adjacent to the first region of the scanning line comprising at least a second transistor, which has having a second gate electrode connected to 10 the scanning line, a second drain electrode connected to a second signal line, and a second source electrode connected to a second pixel electrode, wherein a second overlapping region exists between the second gate electrode and the second source electrode having a second 15 overlapping, an area of the first overlapping being greater than an area of the second overlapping region[[;]]
- wherein the first region is located between the seanning line control circuit and the second region, and an area of the second overlapping region is greater—than that of the first overlapping region by a prodotormined value.
- 25 Claim 6 (Currently Amended): The liquid crystal display of claim 5 wherein the first gate electrode comprises a first block located within the first overlapping region, and the second gate electrode comprises a second block located within the second overlapping region, and an area of the first second block is greater than that of the second first block by the predetermined value.

5

- Claim 7 (Original): The liquid crystal display of claim 6 wherein the first gate electrode further comprises a pair of protective structures located on both sides of the first block for preventing the first block from being separated from the first gate electrode.
- Claim 8 (Currently Amended): The liquid crystal display of claim 5 wherein the first source electrode comprises a third block located within the first overlapping region, and the second source electrode comprises a fourth block located within the second overlapping region, and an area of the third fourth block is greater than an area of the fourth third block by the predetermined value.